

WELCOME TO LEC2 TECH■WEBS



FREE of charge training sessions from July 19th to 23rd

LEC2 in close cooperation with Future Electronics is conducting a series of advanced technical trainings targeted to existing or future Lattice FPGA users.

Course Objective:

LEC2's training and education program is intended to cover the entire Lattice FPGA product range, software tools, design techniques and best practices. Our webinars are characterized by detailed technical content (not a Marketing format) where we present exactly the content that a Lattice FPGA developer needs in his design work.

The series of TechWebs are planned to cover the following subjects:

- FPGA Architectures
- FPGA Design Technology
- Lattice Software Design Suite
- Timing Constraints
- Implementation and Programming
- HDL for Synthesis
- HDL for Simulation
- Debugging Techniques

Overview - July 19th to 23rd

Each Webinar is conducted twice in two different timeslots.

Title	Speaker	Date (CEST*)	Agenda
Timing Constraints sign up	Eugen Krassin LEC2	21.07.2021, 08.30 p.m. 23.07.2021, 10.00 a.m.	Agenda
FPGA Design Technique sign up	Eugen Krassin LEC2	21.07.2021, 10.00 a.m. 22.07.2021, 07.00 p.m.	Agenda
Crosslink NX sign up	Eugen Krassin LEC2	20.07.2021, 07.00 p.m. 22.07.2021, 10.00 a.m.	Agenda
Introduction to FPGA Verification sign up	Hans-Jürgen Schwender TRIAS mikroelektronik GmbH	19.07.2021, 07.00 p.m. 20.07.2021, 10.00 a.m.	Agenda
ModelSim Lattice Edition sign up	Wolfgang Loewer El Camino GmbH	19.07.2021, 10.00 a.m. 21.07.2021, 07.00 p.m.	Agenda

* CEST : Central European Summer Time (GMT+2h)

About LEC2

LEC2 offers a variety of free and paid comprehensive technical training courses for engineers and technicians. Different training formats are available: on customer site, face-to-face, live & recorded webinars, shorties, custom training sessions based on customer needs (check [LEC2 training portfolio](#)).

LEC2 partner with Future Electronics to offer a series of free, comprehensive technical webinar program that is held at regular intervals.